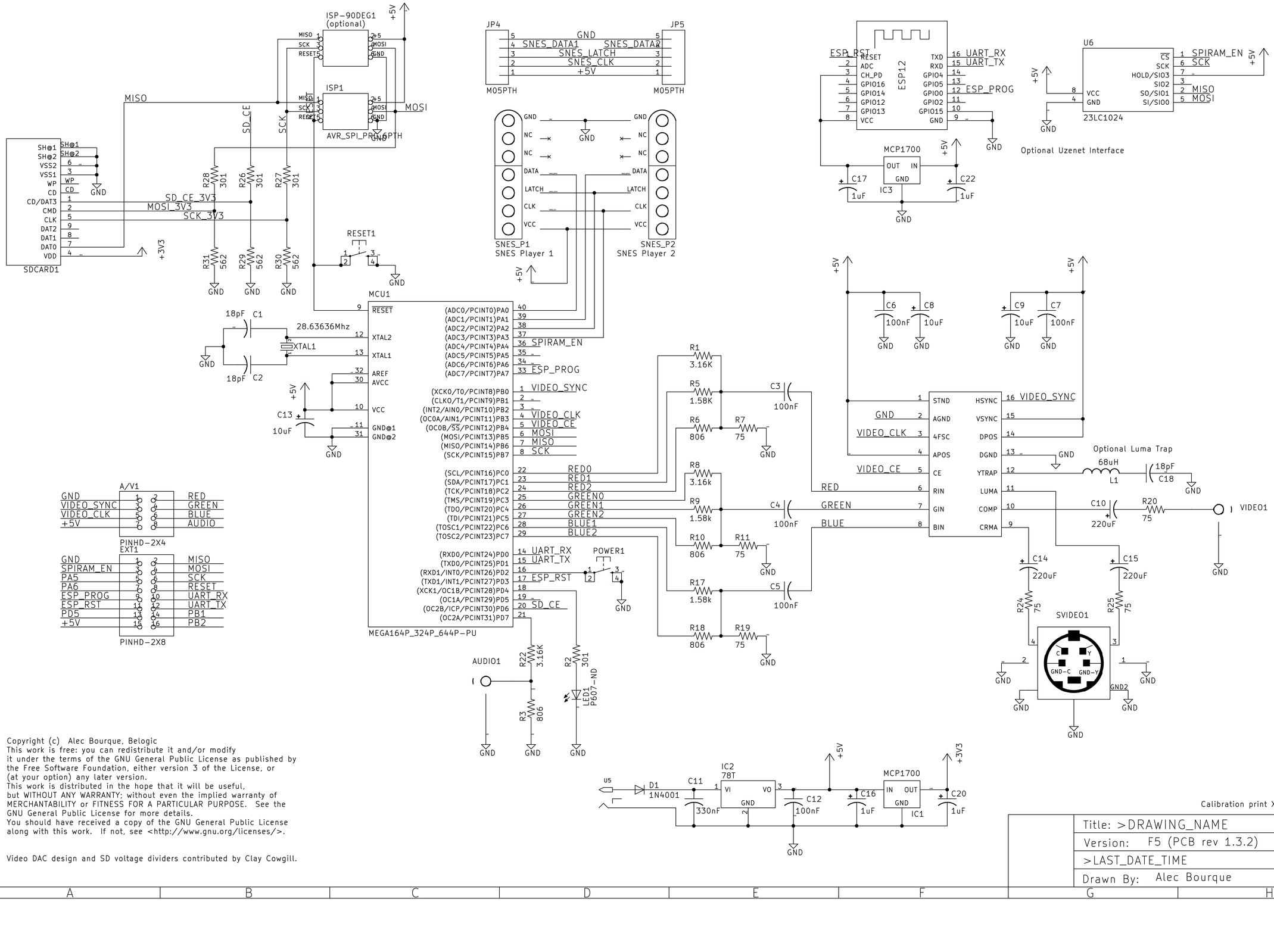


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Video DAC design and SD voltage dividers contributed by Clay Cowgill.

Calibration print X=1.005

Title: >DRAWING_NAME	
Version: F5 (PCB rev 1.3.2)	
>LAST_DATE_TIME	
Drawn By: Alec Bourque	>SHEET